

## Thyristor Modules(Non isolated type)



MTG50A-130A



MTG100A 12



MTG200G 08

### Technical parameter:

#### MTG(AA) MTY (AA)

Type	V <sub>DRM</sub> V <sub>RRM</sub>	I <sub>T(AV)</sub>	I <sub>TSM</sub>	I <sup>2</sup> t	dv/dt	di/dt	I <sub>DRM</sub> I <sub>RRM</sub>	I <sub>GT</sub>	V <sub>GT</sub>	I <sub>H</sub>	V <sub>TM</sub> /I <sub>TM</sub>	V <sub>TO</sub>	r <sub>T</sub>	R <sub>Jc</sub>	T <sub>Jm</sub>	Outlines
	V	T <sub>C</sub> =110°C A	10ms KA	A <sup>2</sup> sX10 <sup>4</sup>	V/us	A/us	mA	mA	V	mA	V /A	V	mΩ	°C/W	°C	
MTx60	200-600	60	2.10	2.24	800	100	6	100	2.5	100	1.35/180	0.80	2.65	0.420	125	Fig17
MTx80	200-600	80	2.70	3.71	800	100	8	100	2.5	100	1.35/240	0.80	1.59	0.250	125	
MTx100	200-600	100	3.40	5.89	800	100	8	100	2.5	100	1.40/300	0.80	1.75	0.240	125	Fig18 Fig19
MTx130	200-600	130	4.50	10.32	800	100	12	150	2.5	100	1.40/390	0.80	1.10	0.200	125	
MTx160	200-600	160	5.60	16.0	800	100	12	150	2.5	100	1.43/480	0.80	1.15	0.150	125	Fig20
MTx200	200-600	200	7.00	25.0	800	100	15	150	2.5	100	1.39/600	0.80	0.70	0.130	125	

#### MTG MTY

Type	V <sub>DRM</sub> V <sub>RRM</sub>	I <sub>T(AV)</sub>	I <sub>TSM</sub>	I <sup>2</sup> t	dv/dt	di/dt	I <sub>DRM</sub> I <sub>RRM</sub>	I <sub>GT</sub>	V <sub>GT</sub>	I <sub>H</sub>	V <sub>TM</sub> /I <sub>TM</sub>	V <sub>TO</sub>	r <sub>T</sub>	R <sub>Jc</sub>	T <sub>Jm</sub>	Outlines
	V	T <sub>C</sub> =90°C A	10ms KA	A <sup>2</sup> sX10 <sup>4</sup>	V/us	A/us	mA	mA	V	mA	V /A	V	mΩ	°C/W	°C	
MTx50	800-1800	50	1.60	1.30	800	100	8	100	2.5	100	1.70/150	0.80	5.41	0.480	125	Fig17
MTx100	800-1800	100	3.20	5.22	800	100	12	100	2.5	100	1.67/300	0.80	2.45	0.250	125	
MTx150	800-1800	150	5.10	13.3	800	100	12	100	2.5	100	1.67/450	0.80	1.74	0.160	125	Fig18 Fig19
MTx200	800-1800	200	6.50	21.5	800	100	20	150	2.5	100	1.62/600	0.80	1.15	0.130	125	
MTx250	800-1800	150	8.50	36.8	800	100	20	150	2.5	100	1.65/750	0.80	1.02	0.100	125	Fig20
MTx300	800-1800	300	9.60	47.0	800	100	25	150	2.5	100	1.58/900	0.80	0.72	0.080	125	

### Circuit Configuration:

